



**For more information, contact:**

Jean-Michel FERNANDEZ  
[fernandez@magillem.com](mailto:fernandez@magillem.com)  
+33 6 34 87 05 11

**Press Release**

FOR IMMEDIATE RELEASE

## Magillem Partners with Imperas

### A winning combination in delivering value to system developers

**Paris, France - February 26th, 2018** - Since 2015, Magillem ([www.magillem.com](http://www.magillem.com)), the leading provider of front-end design xml solutions and best-in-class tools to reduce the global cost of complex designs, has partnered with Imperas ([www.imperas.com](http://www.imperas.com)), which is revolutionizing embedded software development, debug and test for multi-core designs via high-performance virtual platforms, high-level software and system simulation, and open models. Together, Magillem and Imperas provide a unique virtual prototyping solution set, fully based on the IEEE standards IP-XACT and SystemC.

Imperas delivers virtual platforms (virtual prototypes) spanning ultra-fast simulation, advanced debug solutions, and models including processors from Arm, RISC-V, MIPS, Altera, PowerPC, Renesas, Synopsys ARC, Xilinx and others. The Open Virtual Platforms (OVP) initiative, at [www.ovpworld.org](http://www.ovpworld.org), makes these models available as open source. Imperas combines high-performance models with powerful simulation, debug and test tooling to perform architectural analysis, early software development and more comprehensive embedded software test, analysis and optimization across many processor cores and the full spectrum of operational scenarios.

Thanks to this partnership, Magillem offers a complete front-end design and verification flow from SystemC down to RTL simulation, including software simulation and debug. Supporting a reuse methodology, the Imperas processor and peripheral models are packaged in IP-XACT and assembled in the Magillem tool suite. The constructed virtual platform can be progressively refined into a RTL platform, in a Continuous Integration (CI) flow.

*"The wide support of OVP fast processor core models, together with Imperas tools for hardware-dependent software analysis, perfectly complements our Executable Specification solution,"* said Jean-Michel Fernandez, Embedded Systems Product Line director of Magillem. *"The tight integration of Imperas technology with Magillem tools will help our customers to seamlessly execute their embedded software on their hardware specification."*

Simon Davidmann, Imperas CEO, commented, *"The complexity of embedded systems, in particular the software for those systems, is growing exponentially. With the inclusion of virtual platform-based technology in mainstream embedded software development methodologies, correct-by-construction tools like those from Magillem are also vital. The combination of Magillem and Imperas technologies helps tame the complexity for embedded system developers."*



# # #

### About Magillem

**Magillem** has been the pioneer, since 2006, in software leveraging business content for top tier semiconductors and embedded systems accounts worldwide. Magillem has been listed on Euronext Paris since 2009 (FR0010827741) and is trusted by numerous clients like Altera, Samsung, Qualcomm, NXP, ST Microelectronics, Texas Instruments, Thales...

Magillem has 60 employees, including 48 engineers and PhDs in Research & Development alone. With an office in Tokyo, a subsidiary in Korea, one in China, 3 agencies in the United States (New York, Austin and the San Francisco Bay area) and 8 distributors in Asia and Israel, its footprint extends to all major countries. For more information, please visit [www.magillem.com](http://www.magillem.com)

### About Imperas

**Imperas Software Ltd.** was founded in 2008 to develop and deliver embedded software development systems. The company's comprehensive product line enables the rapid creation of high-performance virtual platforms and the efficient development of embedded software utilizing those platforms. Imperas' technology allows for software engineering schedules to be significantly reduced while improving the quality of products relying on embedded systems. In 2008 Imperas founded the Open Virtual Platforms (OVP) consortium to improve the availability of open model libraries and virtual platform infrastructure. Leading communications, automotive, consumer electronics and embedded processor companies rely on Imperas for the development of their electronic products. The company's corporate headquarters is located near Oxford, UK and it maintains support and sales organizations in Silicon Valley, California and Tokyo, Japan. For more information about Imperas, please go to [www.imperas.com](http://www.imperas.com)